

FIG. 1

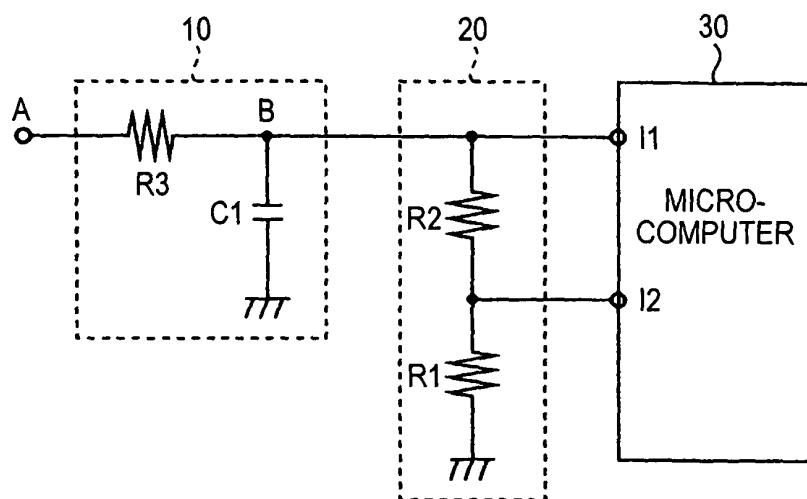


FIG. 2A

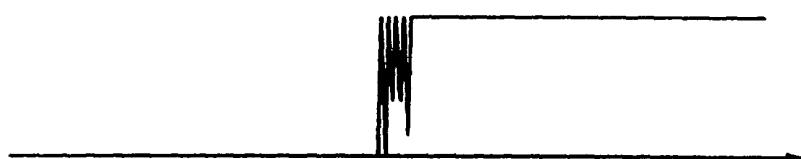


FIG. 2B

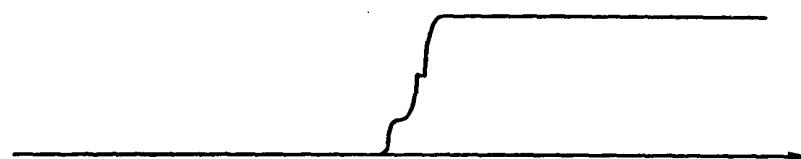


FIG. 3

INPUT VOLTAGE	I1	I2	PROCESS RESULT
$V_i \geq (1 + R_2/R_1) \cdot V_{th}$	Hi	Hi	Hi
$(1 + R_2/R_1) \cdot V_{th} > V_i \geq V_{th}$	Hi	Lo	UNCHANGED
$V_{th} > V_i$	Lo	Lo	Lo

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FIG. 4A

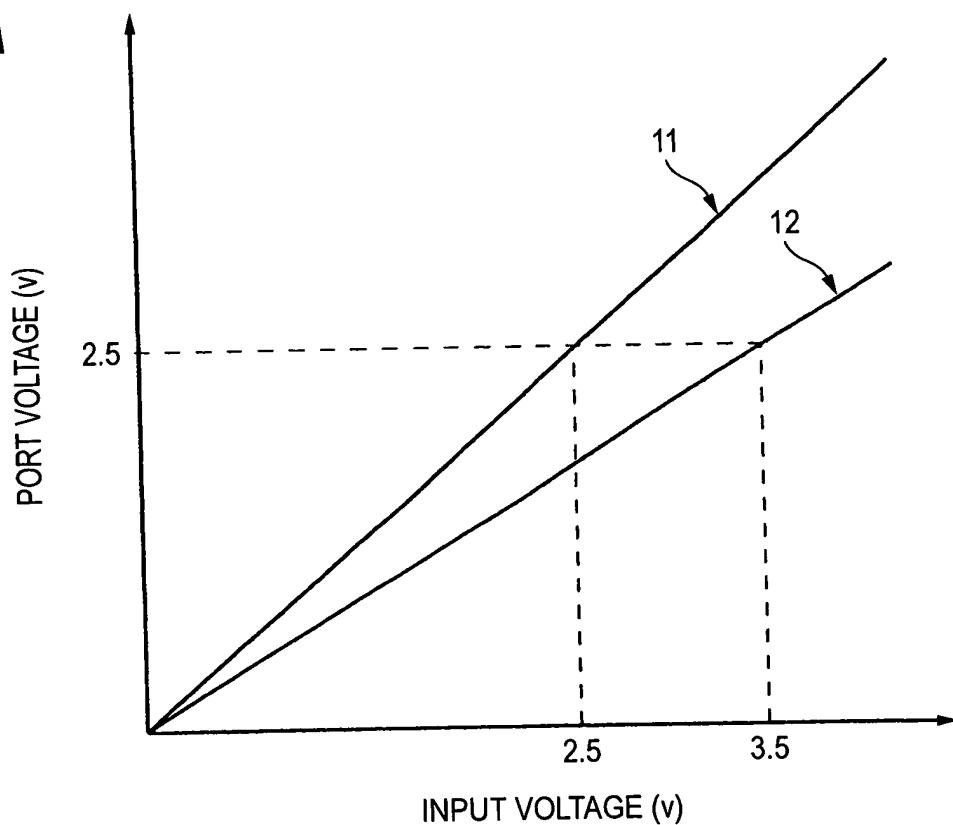


FIG. 4B

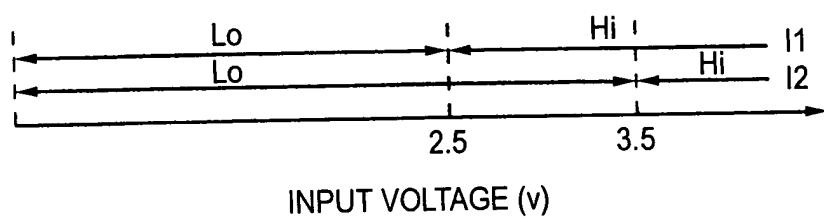


FIG. 4C

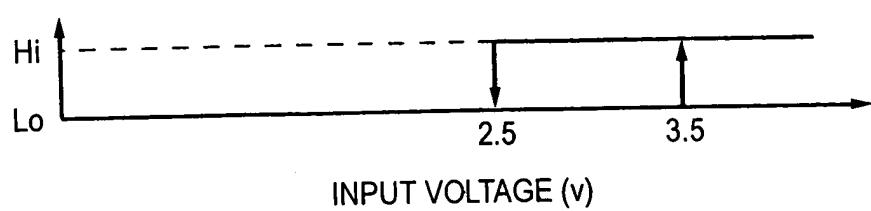


FIG. 5

